

# United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,245	06/20/2003	Sandeep Bhatia	14532US01	5543
Christopher C.	7590 12/19/2006 Winslade	EXAMINER		
McAndrews, Held & Malloy, LTD.  34th Floor 500 West Madison Street Chicago, IL 60661			VO, TUNG T	
			ART UNIT	PAPER NUMBER
			2621	
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS 12/19/2006			PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

	Application No.	Applicant(s)				
	10/600,245	BHATIA, SANDEEP				
Office Action Summary	Examiner	Art Unit				
·	Tung Vo	2621				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DATE - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNION  36(a). In no event, however, may a reviil apply and will expire SIX (6) MON, cause the application to become AE	CATION.  poly be timely filed  THS from the mailing date of this communication.  ANDONED (35 U.S.C. § 133).				
Status		•				
Responsive to communication(s) filed on  2a) ☐ This action is <b>FINAL</b> . 2b) ☑ This  3) ☐ Since this application is in condition for allowar closed in accordance with the practice under E	· · · · · · · · · · · · · · · · · · ·	•				
Disposition of Claims						
4)  Claim(s) 1-14 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5)  Claim(s) is/are allowed. 6)  Claim(s) 1-14 is/are rejected. 7)  Claim(s) is/are objected to. 8)  Claim(s) are subject to restriction and/or	vn from consideration.					
Application Papers						
9) ☐ The specification is objected to by the Examine 10) ☑ The drawing(s) filed on 29 December 2003, and Examiner.  Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) ☐ The oath or declaration is objected to by the Examiner.	d 20 June 2003 is/are: a)  drawing(s) be held in abeyar  ion is required if the drawing	ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
	ł					
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date	Paper No(s	ummary (PTO-413) )/Mail Date formal Patent Application 				

Application/Control Number: 10/600,245

Art Unit: 2621

## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-14 are rejected under 35 U.S.C. 102(e) as being anticipated by Kono et al. (US 2001/0005398).

Re claims 1, 5, and 12, Kono discloses a system (fig. 6) for displaying images on a display, said system comprising:

a decoder (52 of fig. 6) for decoding encoded images and parameters associated with the images, thereby resulting in decoded images and decoder parameters associated with the decoded images;

image buffers (53 of fig. 6) for storing the decoded images;

- a queue (57 of fig. 6) for storing indicators indicating images to be displayed; and
- a display engine (55 of fig. 6) for presenting the images indicated by the queue for display.

Re claims 2, 6, and 13, Kono further discloses parameter buffers (53f-53h of fig. 6) for storing the decoded parameters associated with the images.

Application/Control Number: 10/600,245

Art Unit: 2621

Re claims 3, 7, and 14, Kono further discloses wherein the display engine (55 of fig. 6) presents the images indicated by the queue for display by receiving the decoded parameters and displaying the decoded images based on the decoded parameters (DISPLAY UNIT of fig. 6).

Re claim 4, Kono further discloses wherein the decoder comprises a first processor (54 of fig. 6) and the display engine comprises a second processor (DISPLAY UNIT of fig. 6).

Re claims 8-11, Kono discloses a circuit (fig. 6) for displaying images on a display, said circuit comprising:

a first processor (52 of fig. 6);

a first memory (54 of fig. 6, Note the decode control section holds the initial decode starting instruction and slice layer decode starting instruction, which is considered as a memory) operably coupled to the first processor, said first memory storing a plurality of instructions for execution by the first processor,

wherein the plurality of executable instructions cause:

decoding (63 of fig. 6) encoded images and parameters associated with the images, thereby resulting in decoded images and

decoder parameters associated with the decoded images (71 of fig. 6); storing the decoded images (53a-53b of fig. 6);

storing indicators indicating images to be displayed in a queue (57 of fig. 6); and a second processor (55 of fig. 6) operably coupled to the queue;

a second memory (53f-53h of fig. 6) operably coupled to the second processor, said second memory storing a plurality of instructions for execution by the second processor (68 and 73 of fig. 6),

Application/Control Number: 10/600,245

Art Unit: 2621

wherein the plurality of executable instructions cause:

presenting the images indicated by the indicators for display (68, 74 of fig. 6); wherein the instructions causing presenting the images further comprise instructions causing receiving the decoded parameters and displaying the decoded images based on the decoded parameters (DISPLAY UNIT of fig. 6).

## Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Goto et al. (US 6,633,339) discloses a method and device for seamless decoding video stream including streams having different frame rates.

## **Contact Information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tung Vo whose telephone number is 571-272-7340. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mehrdad Dastouri can be reached on 571-272-7418. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/600,245 Page 5

Art Unit: 2621

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000

**Primary Examiner** 

Art Unit 2621